OPAL-RT's new Schematic Editor is an intuitive user interface for building power electronics and is a part of eHS, the fastest FPGA-Based power electronics toolbox in the industry. It provides an advanced graphical editor and is integrated with OPAL-RT’s & NI’s real-time simulation platforms.

- **Design power electronics models with ease through drag and drop components**
- **Interact with a streamlined GUI, and a fast simulation workflow to bring models to real-time simulation**
- **Manage analog and digital I/O assignment through the interface**

**Button Header**
Complete diagram actions quickly and easily.

**Library & Model Browser**
Integrate power electronics models with drag and drop components.

**Work Area**
Easily assembled and manipulated models.

**Parameters Side Bar**
Edit parameters of the selected components.
OPAL-RT’s Schematic Editor: Flexible & Intuitive

Library & Model Browser
Easily integrate power electronics models with drag and drop components.

Parameter Sets
Select and apply parameter sets to test the model under various conditions during simulation.

Real-Time Simulator Setup
Access Setup for connection of analog and digital I/Os to the circuit measurements and control.

eHS: The Fastest FPGA-Based Power Electronics Toolbox in the Industry

Integrated directly with both OPAL-RT’s & NI’s real-time simulation platforms, eHS (OPAL-RT’s FPGA-based Power Electronics Toolbox) is a powerful simulation tool for Hardware-in-the-Loop (HIL) testing.

eHS easily enables the running of test sequences and on-the-fly changes to simulation parameters by using the Test Scenario feature. It allows the test engineer to jump from one set of component values to the next without stopping the simulation, and is the perfect system for all types of electrical conversion test applications.

From Modelling to Real-Time Simulation in Three Steps

1. Develop your power electronics diagram with Schematic Editor, or with your favorite circuit editor*.  
2. Then, configure your I/O channels and compile your model for sub-microsecond time steps.  
3. Finally, execute the real-time simulation and perform manual and automated tests.

* Supported circuit editors: Simscape Electrical™, PLECS, PSIM and NI Multisim.