Our clients identified the need and made the requests. And we followed through. Through listening to the market's needs, we've evolved:

- **An easier workflow, and a more intuitive modeling experience:**
  - Use a web-based graphical editor integrated with a powerful real-time database.

- **A power electronics solver that supports microgrid integration:**
  - Simulate a circuit with 1,200 nodes in less than 3 us with 3 FPGAs... a world record.
  - Simulate transformers with saturation characteristics. *
  - Simulate transmission lines. *

- **A bold new step in sampling that enables faster PWM integration in power electronics solving:**
  - Simulate power electronic converters with PWM frequency up to 200 kHz using patent-pending time-stamping solver with 5 ns accuracy.

* for Beta users only

OPAL-RT's new Schematic Editor is a streamlined user interface designed to provide you with the ability to work with your preferred modeling package—or ours. For use with the simplest to the most complex models, our Schematic Editor is tightly-integrated in eHS on our workhorse platform, RT-LAB.

**Both user-friendly and powerful, OPAL-RT’s new Schematic Editor:**

- **Creates** professional power electronic circuits
- **Integrates** seamlessly with power electronics solving
- **Manages** analog and digital I/O assignment through the interface

OPAL-RT's Schematic Editor enables users to bring power electronics models to full real-time simulation using its full-featured block set.

Learn more at: www.opal-rt.com
OPAL-RT has now successfully applied eHS’ core strengths to a challenging context—the microgrid

In today’s power grid, alternative power sources such as wind, solar, and stored power are rapidly becoming viable substitutes for more traditional energy production methods.

These innovations have created the real possibility of operating on a fully autonomous islanded grid, as well as increasing the popularity of microgrids, small-scale power grids capable of operating independently from the main grid.

eHS now expands its capabilities through simulation of a complex microgrid—with more than 400 distributed nodes—entirely on one FPGA.

The number of distributed nodes is now extensible, through daisy-chaining machines and thus increasing power to the nth degree.

Coupling multiple real-time simulators through the low latency FPGA increases the size of the distribution network to over 1,200 nodes simulated in a time step of 3 μs.

THE MARKET EVOLVES AND WE EVOLVE ALONG WITH IT—FOR OVER 15 YEARS NOW. OPAL-RT DELIVERS THE INDUSTRY’S MOST POWERFUL AND INTUITIVE FPGA-BASED REAL-TIME SOLUTIONS.